

Fig. 1A

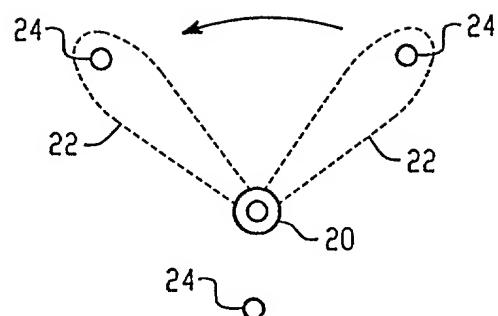


Fig. 1B

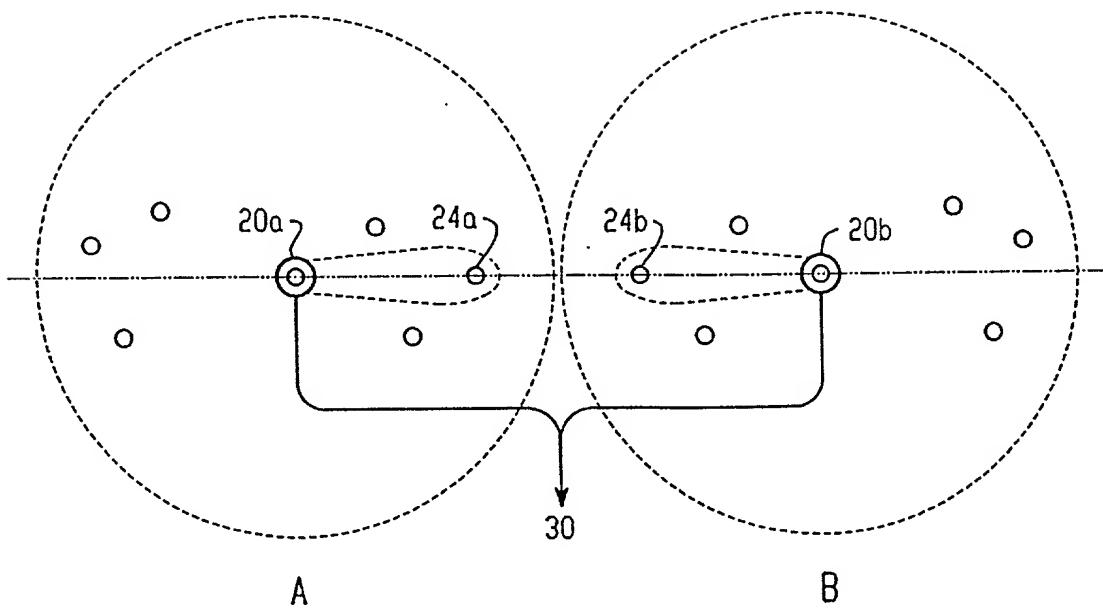


Fig. 1C

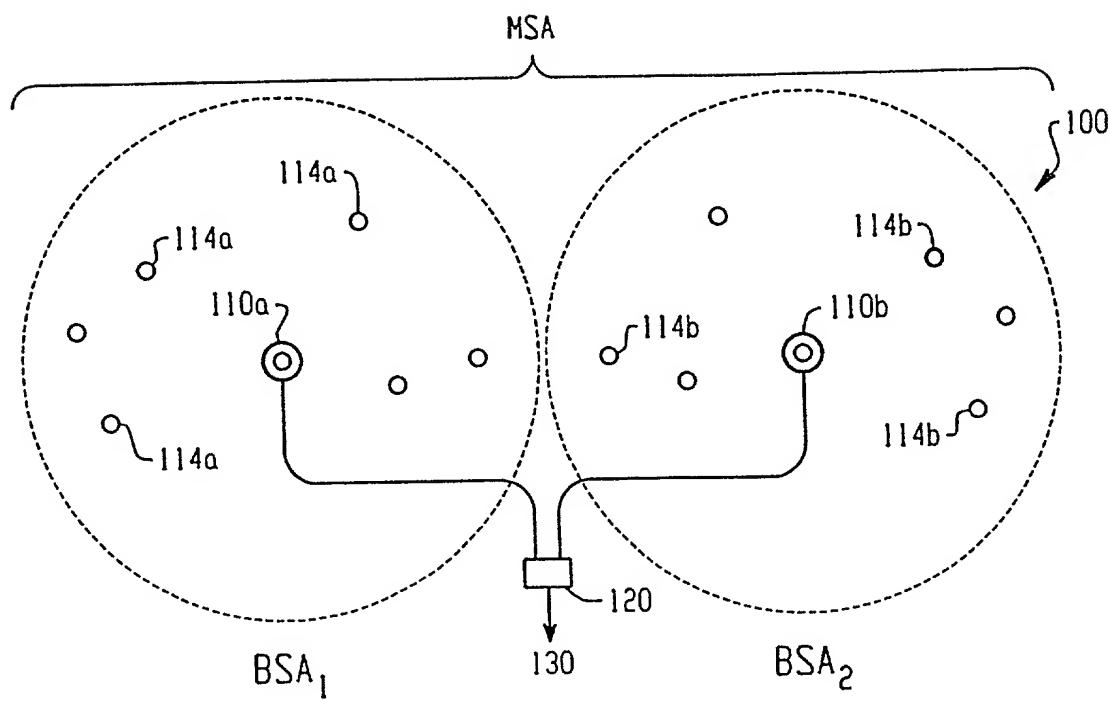


Fig. 2A

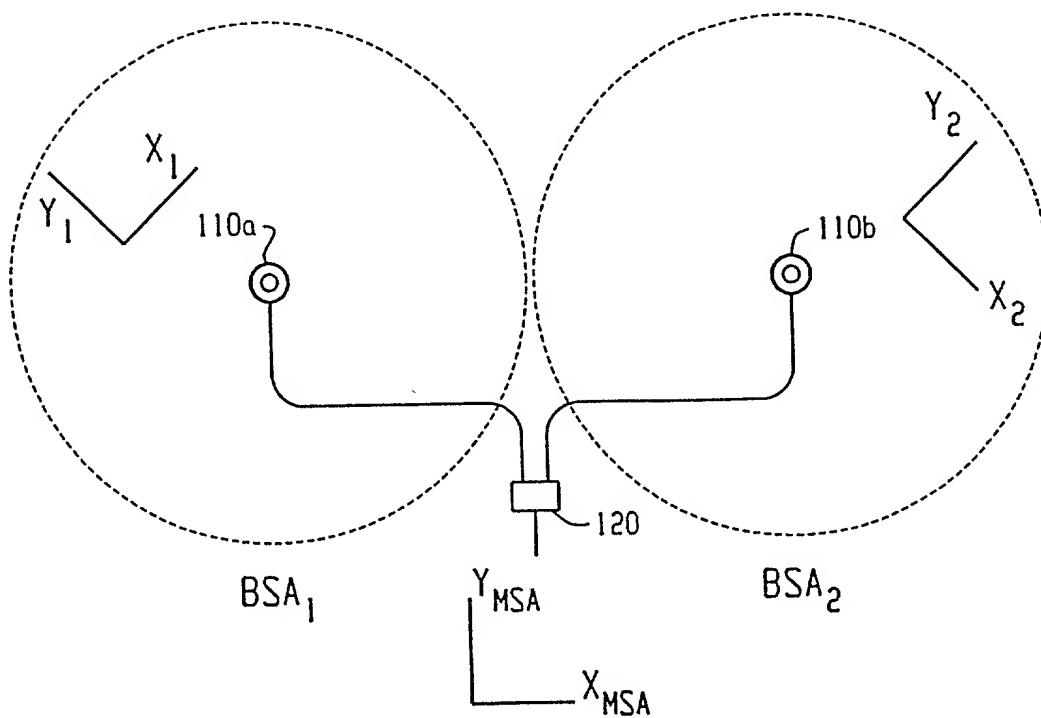
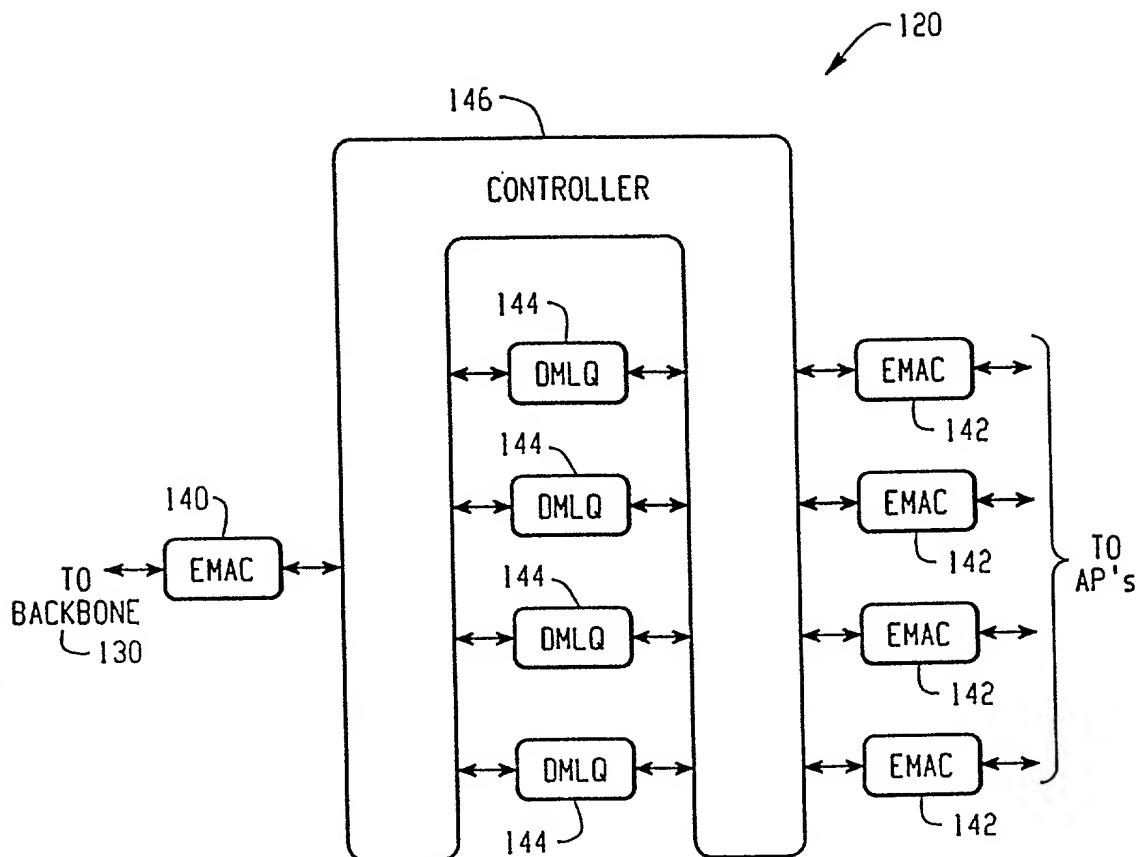


Fig. 2B



DMLQ-DUAL-MULTILEVEL QUEUE
EMAC-ETHERNET MAC

Fig. 3

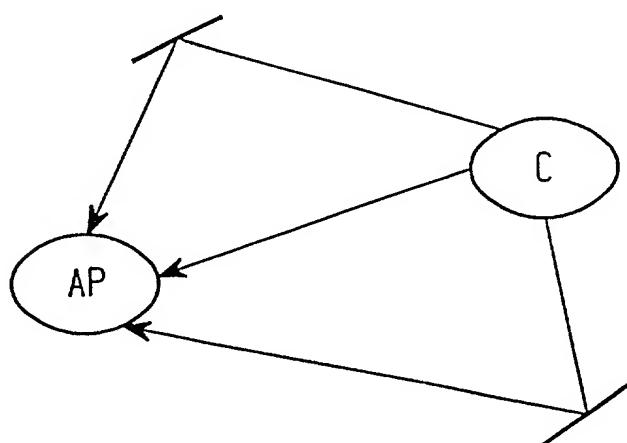


Fig. 7

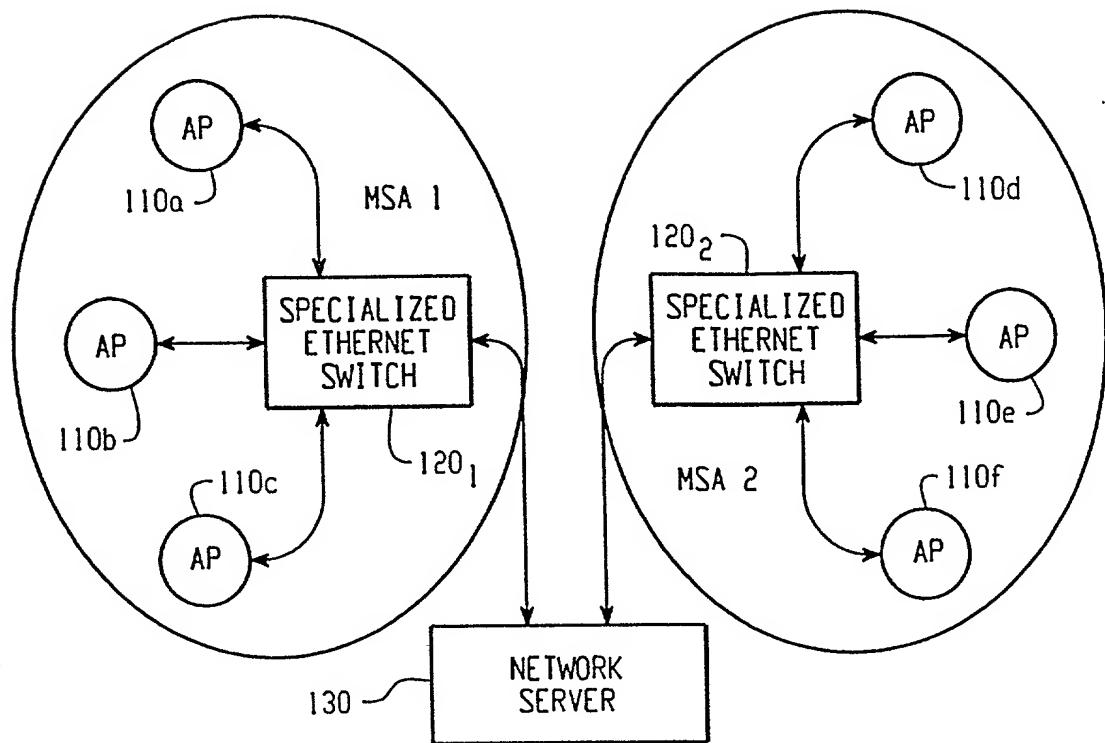


Fig. 4

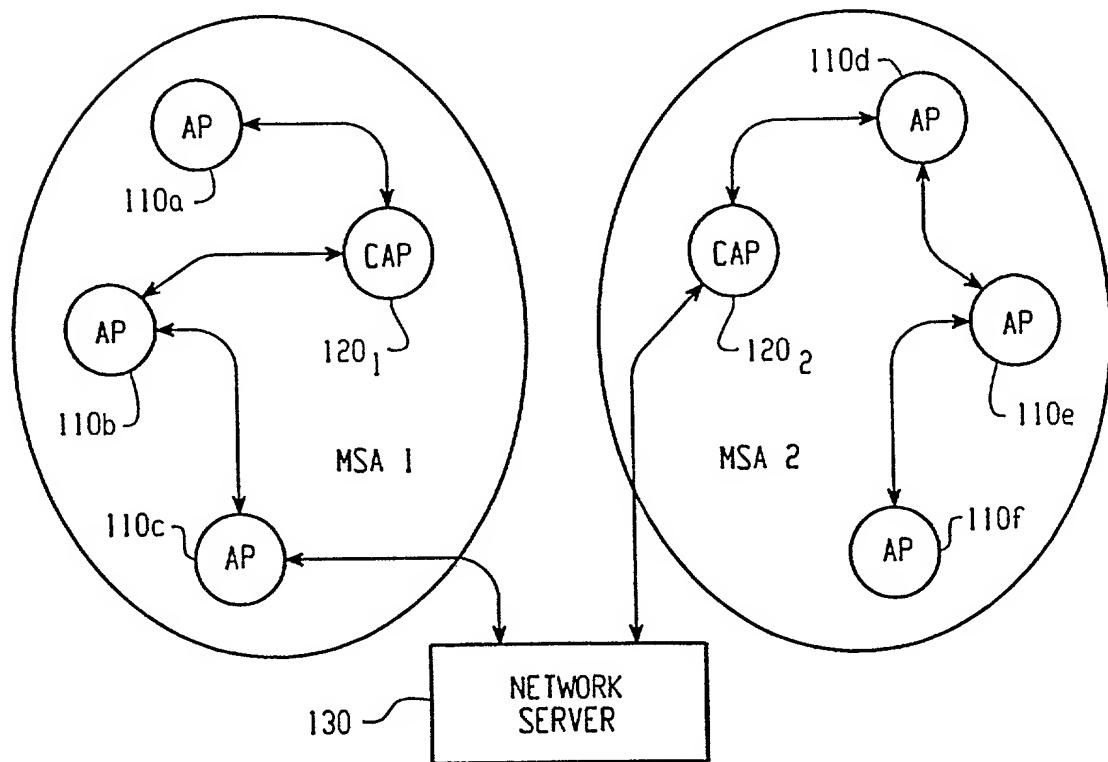


Fig. 5

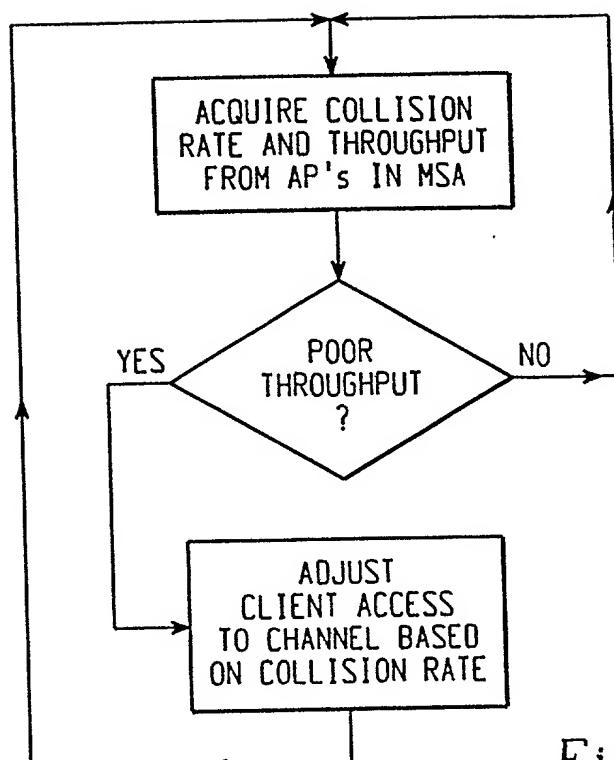


Fig. 6A

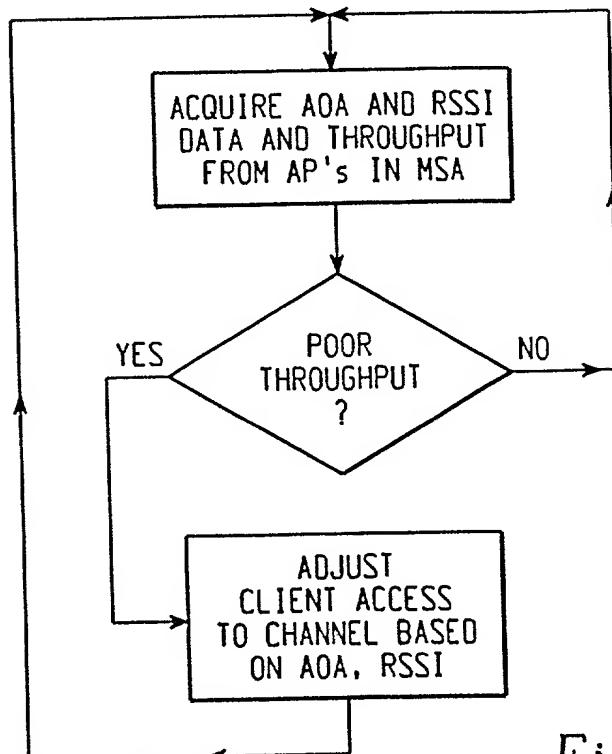


Fig. 6B